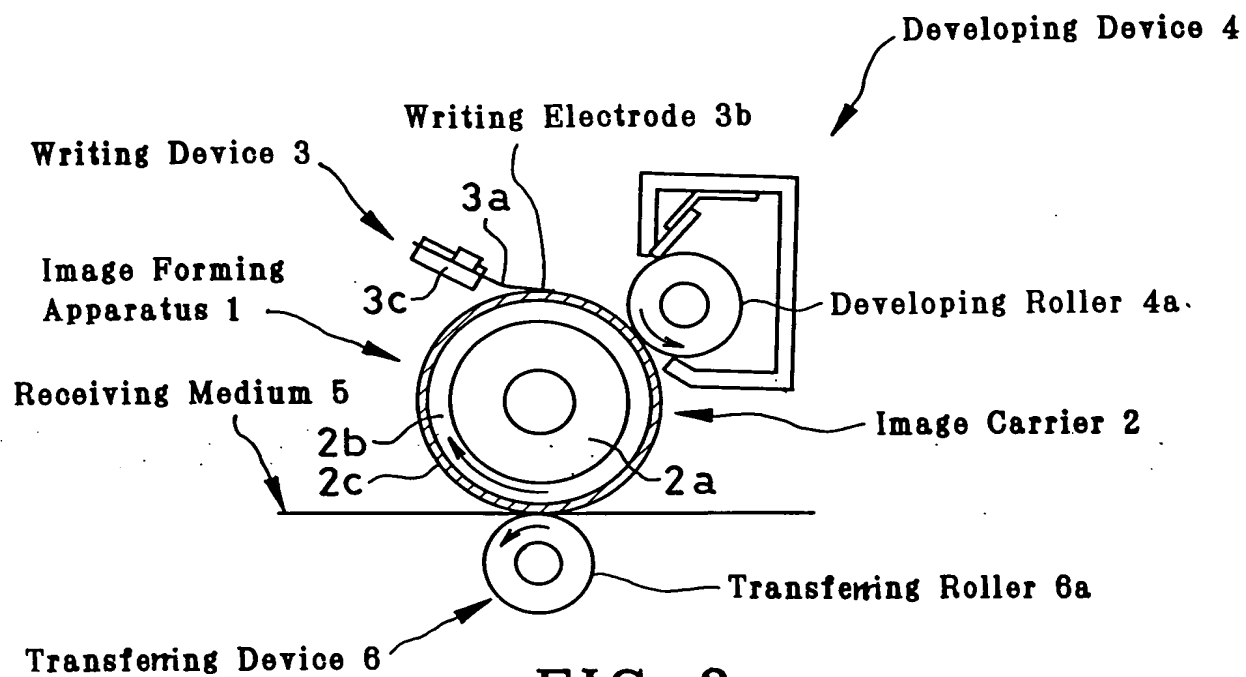


**FIG. 1**



**FIG. 2**

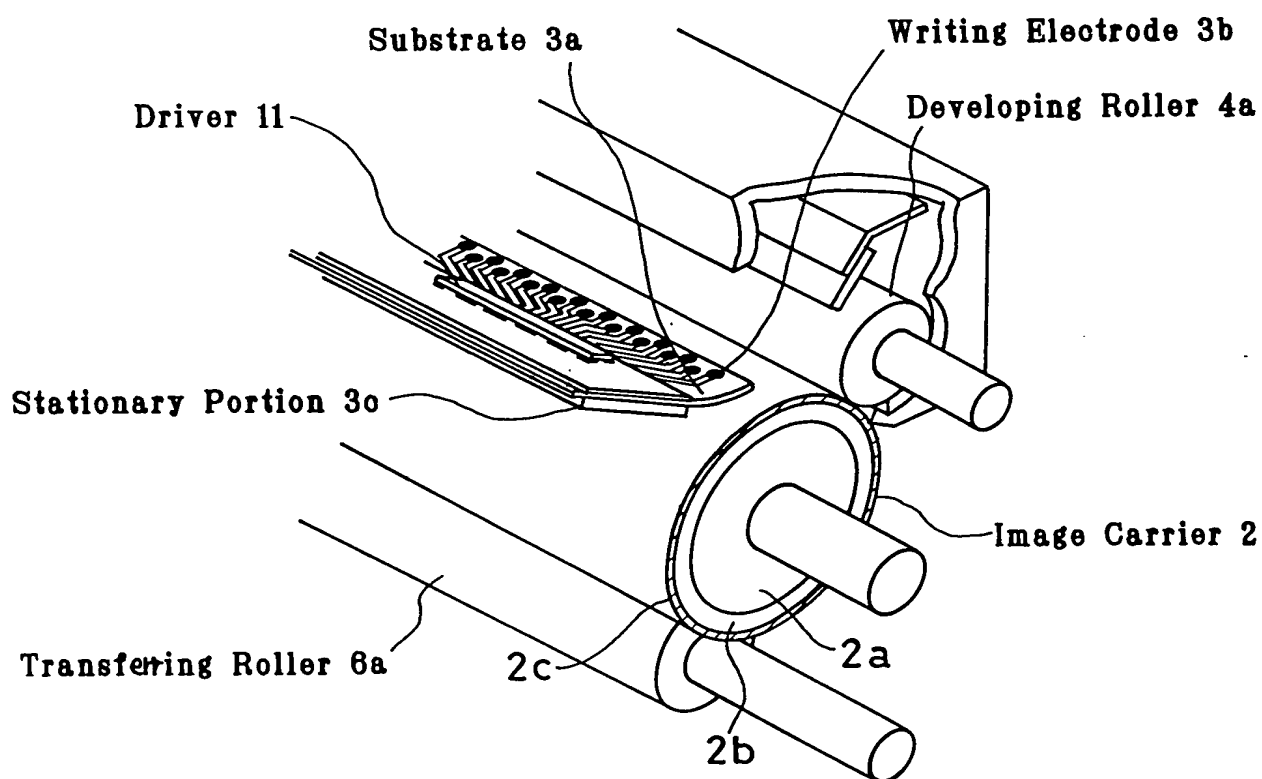


FIG. 3(a)

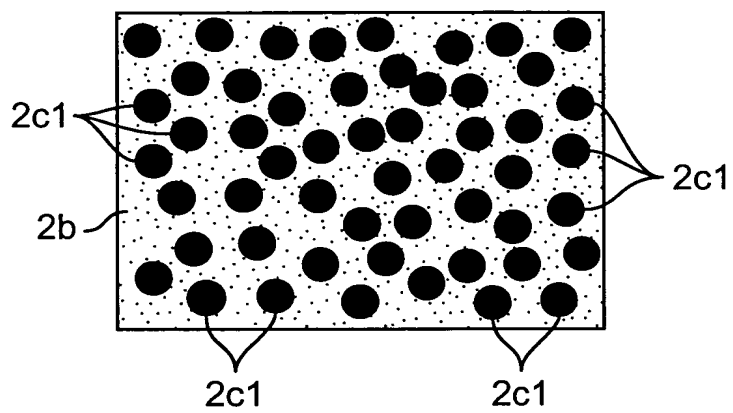
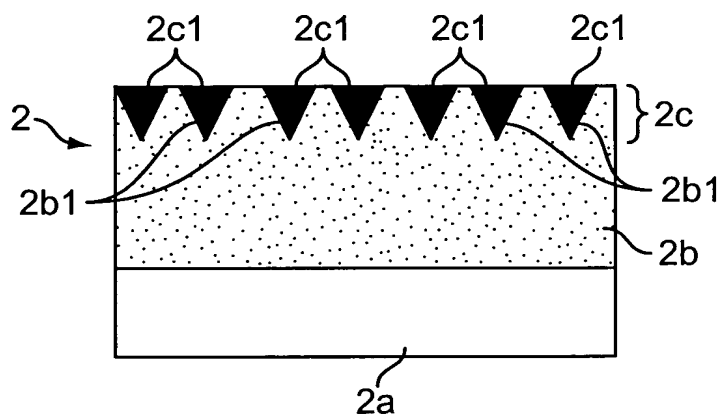


FIG. 3(b)



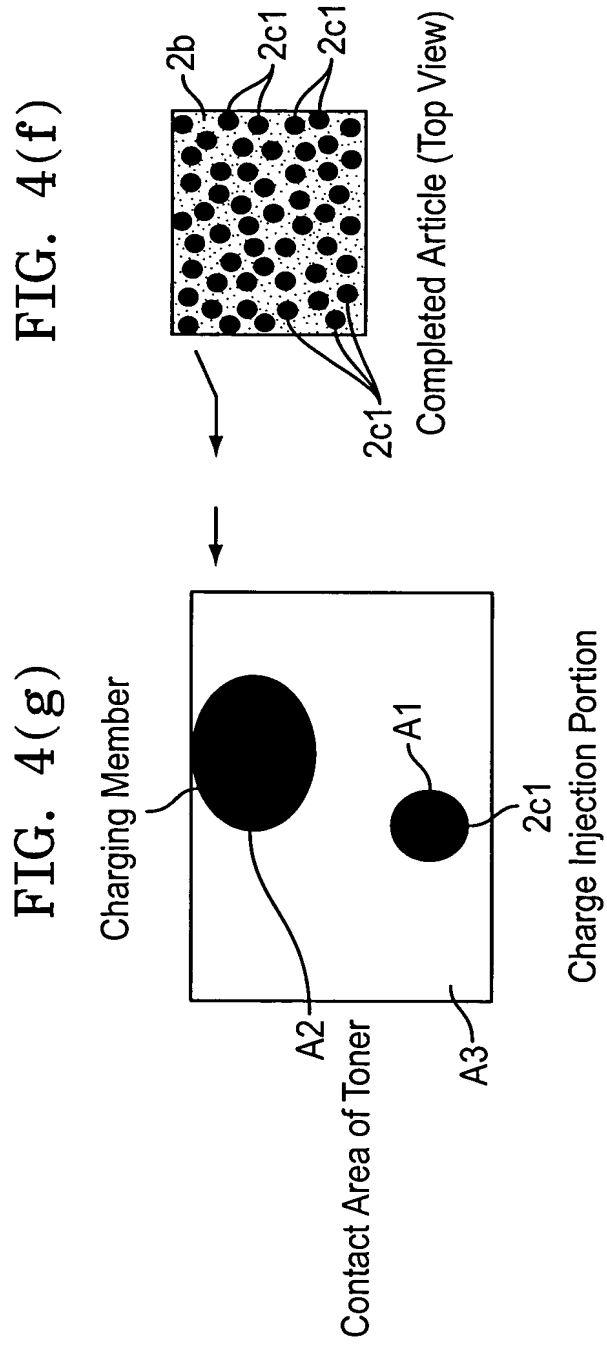
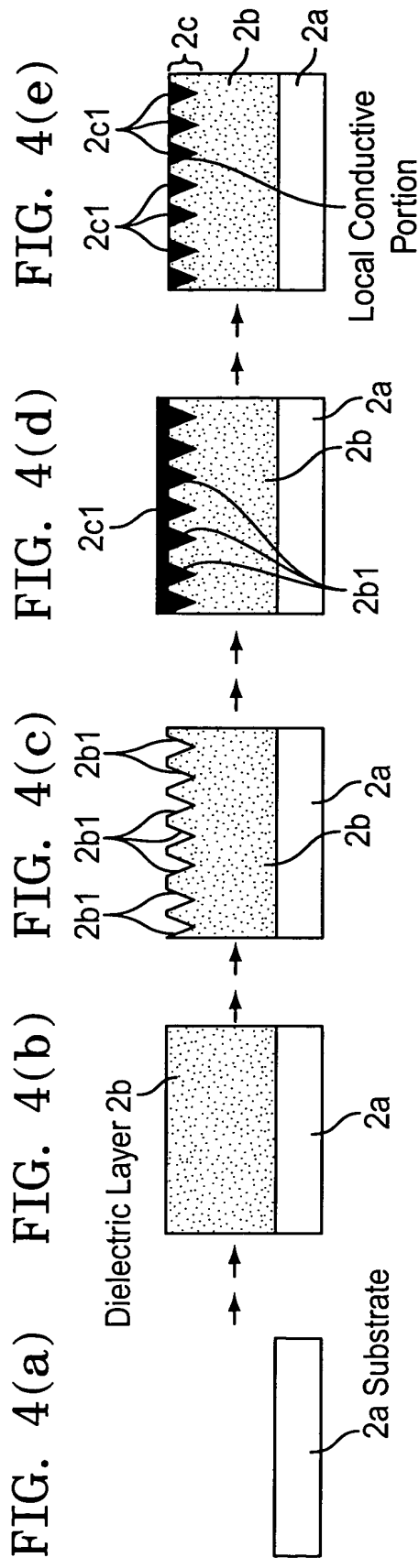


FIG. 5(a)

FIG. 5(b)

FIG. 5(c)

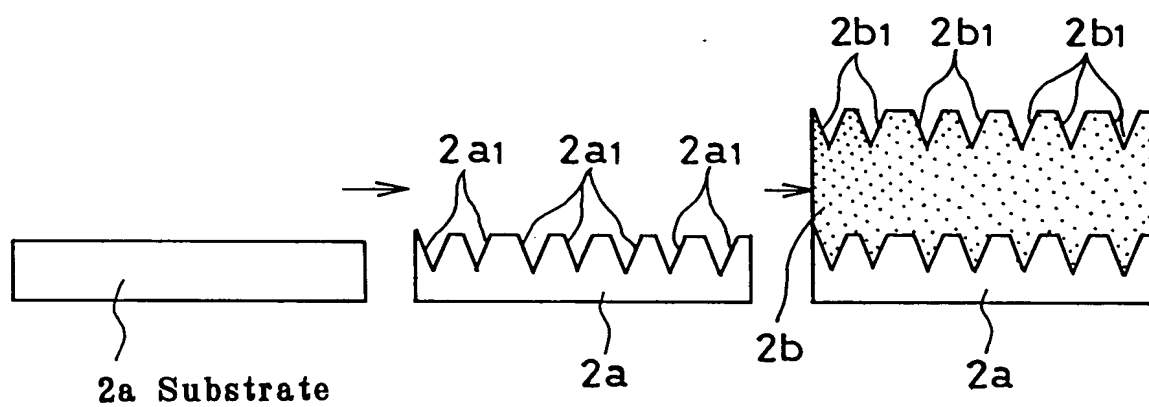


FIG. 6(a)

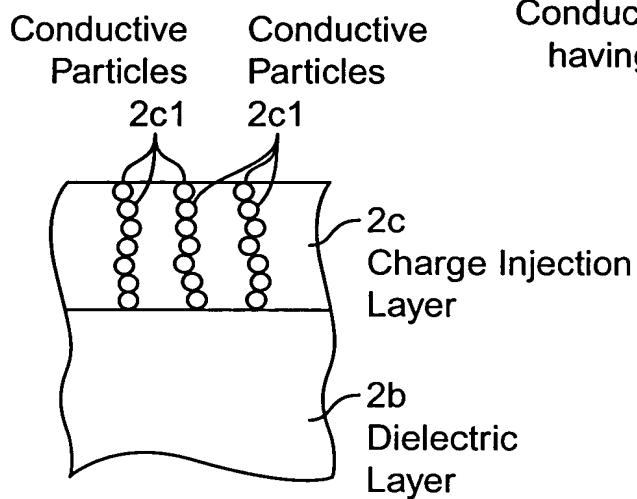


FIG. 6(b)

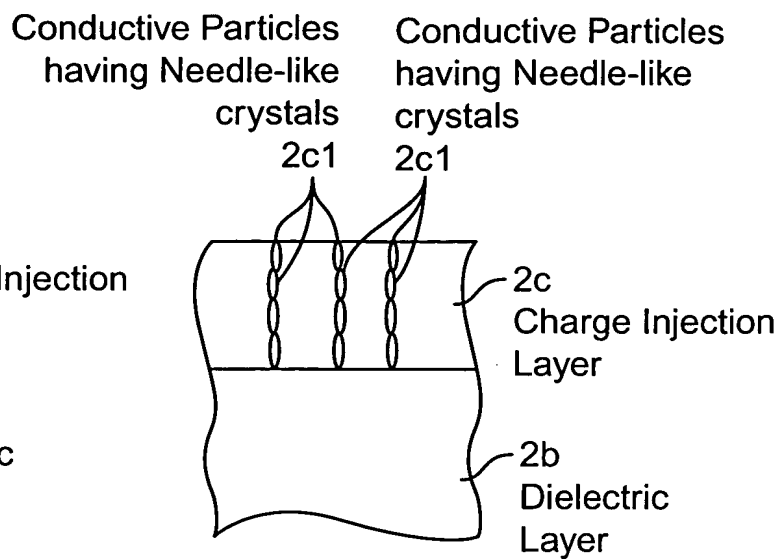


FIG. 7(a)

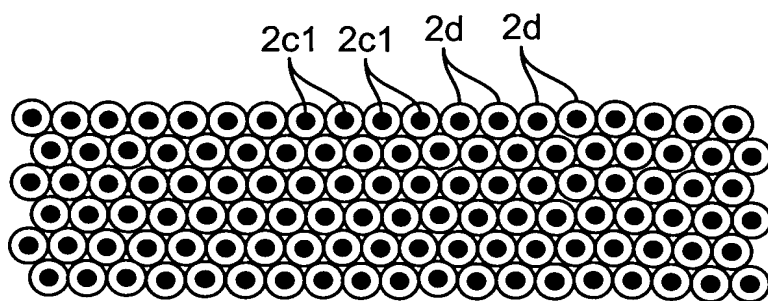


FIG. 7(b)

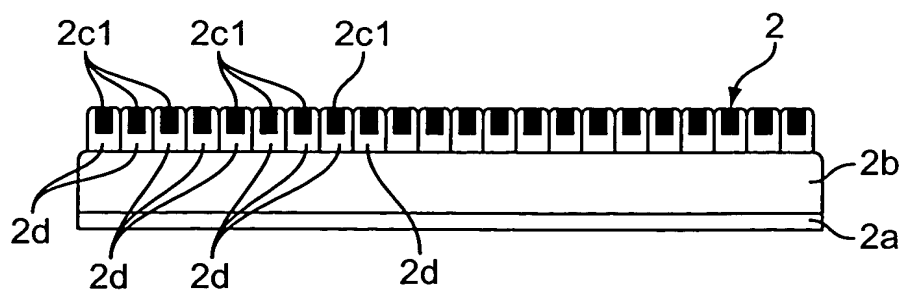


FIG. 8(a)

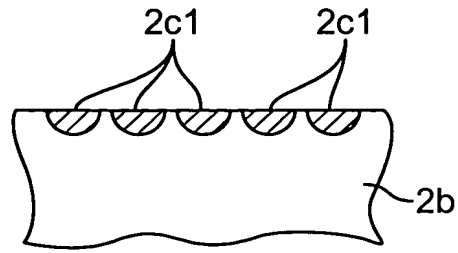


FIG. 8(b)

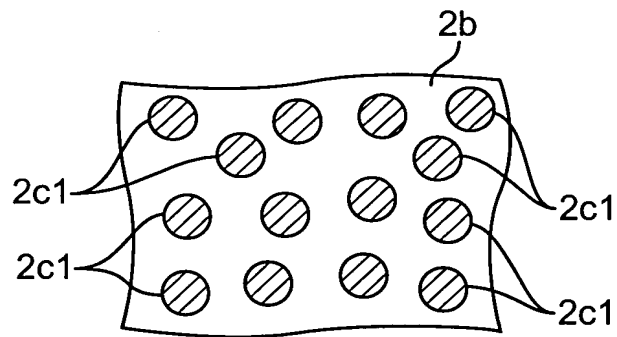


FIG. 9(a)

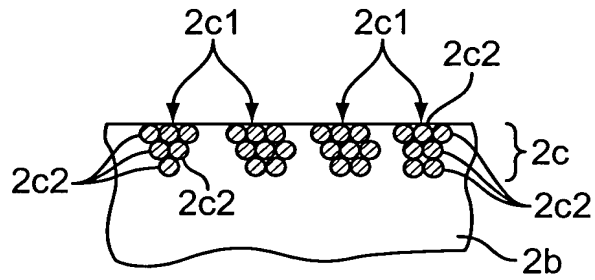
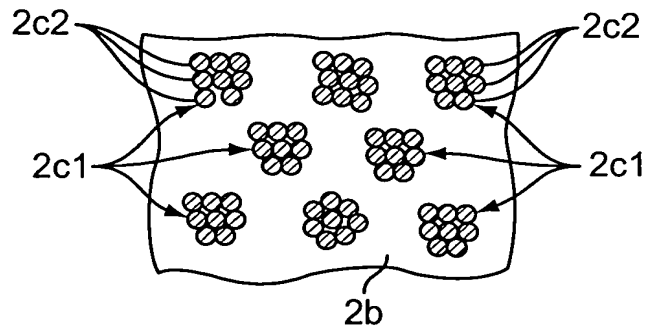


FIG. 9(b)



Line Data Signal  
Writing Timing Signal  
High Voltage Power

## Writing Timing Signal

## High Voltage Power

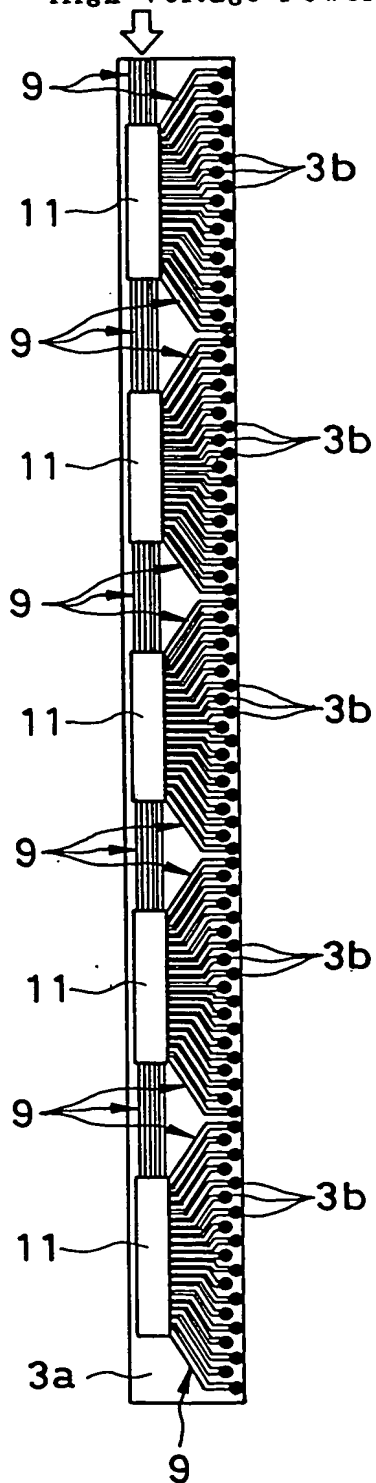
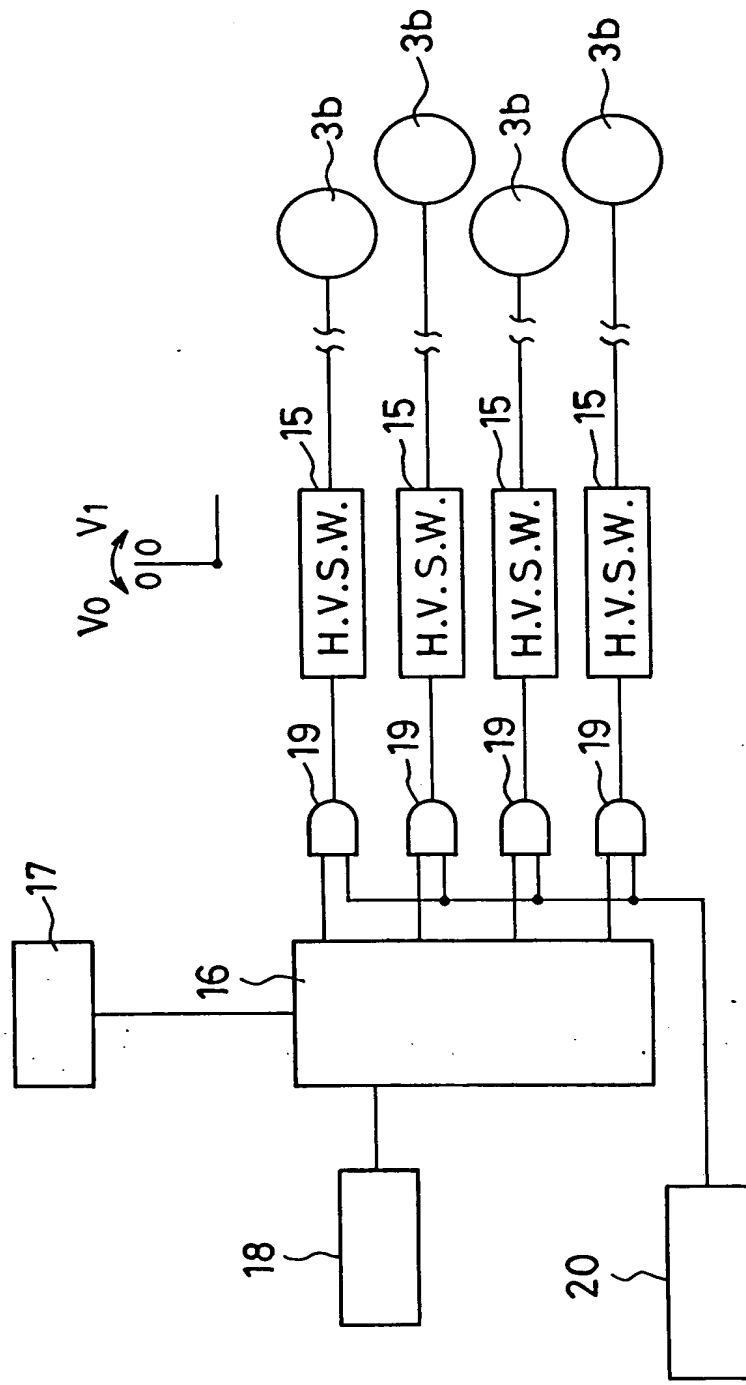
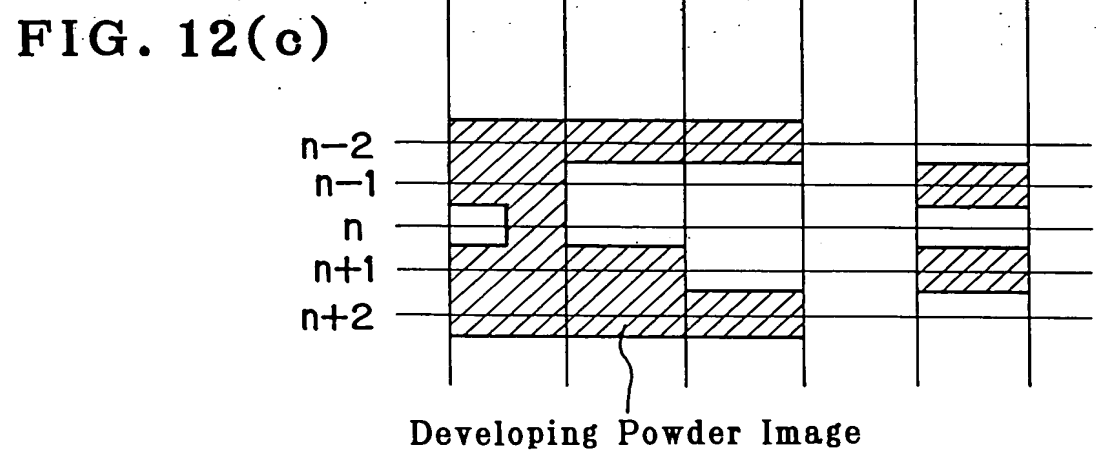
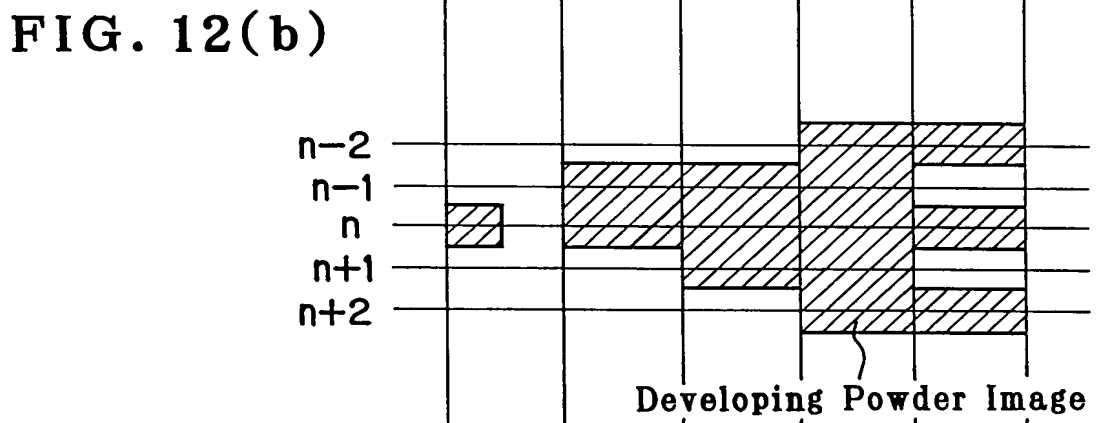
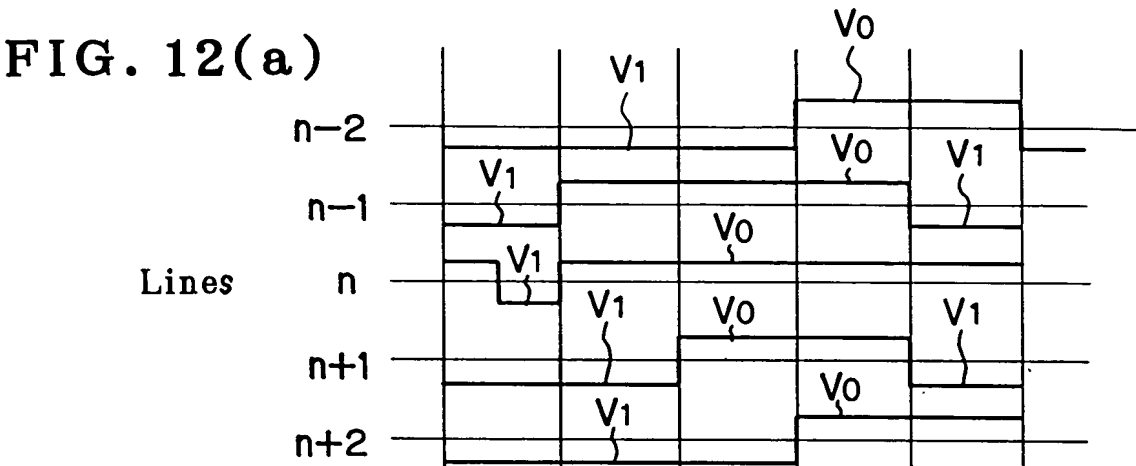


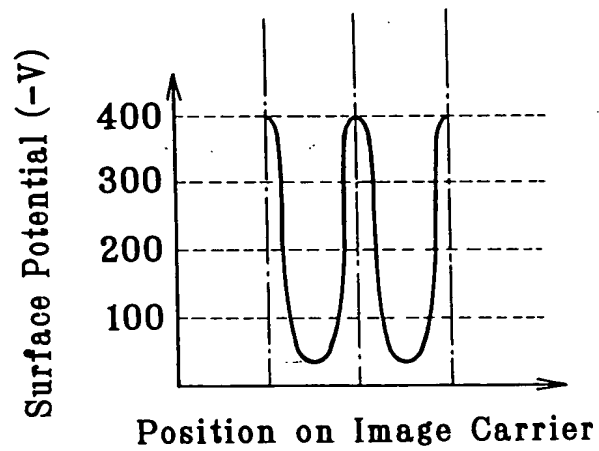
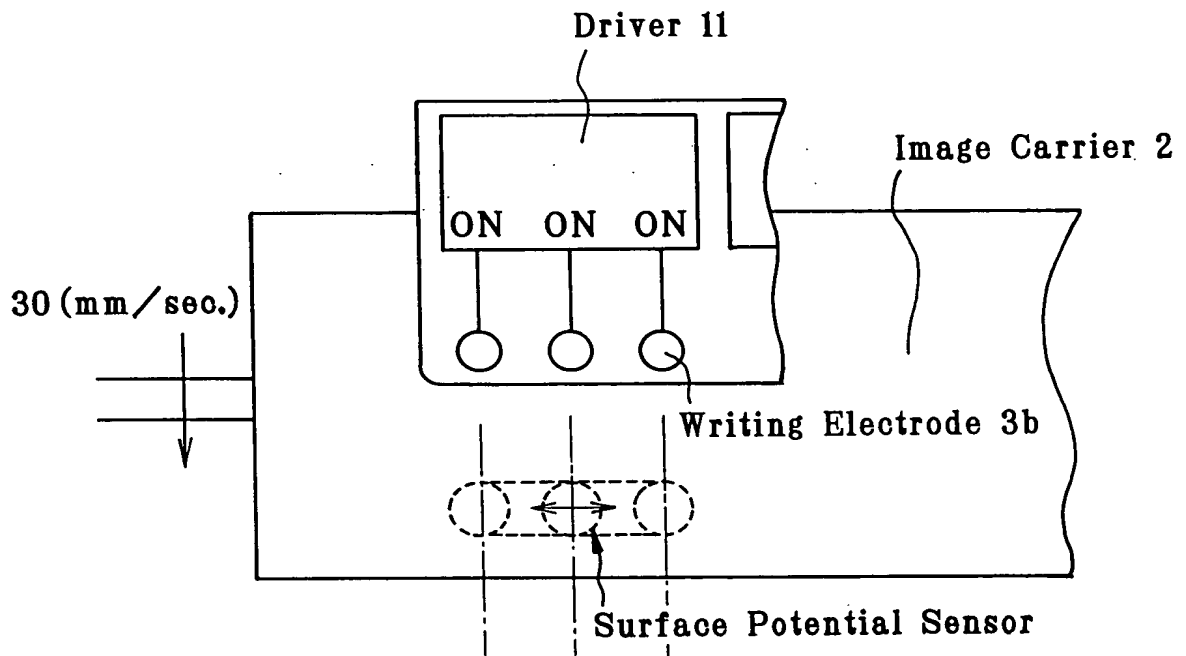
FIG. 11





# FIG. 13

Example (1), Thickness of Charge Injection Layer :  $1\mu\text{m}$



**FIG. 14**

**Example (2), Thickness of Charge Injection Layer :  $10\mu\text{m}$**

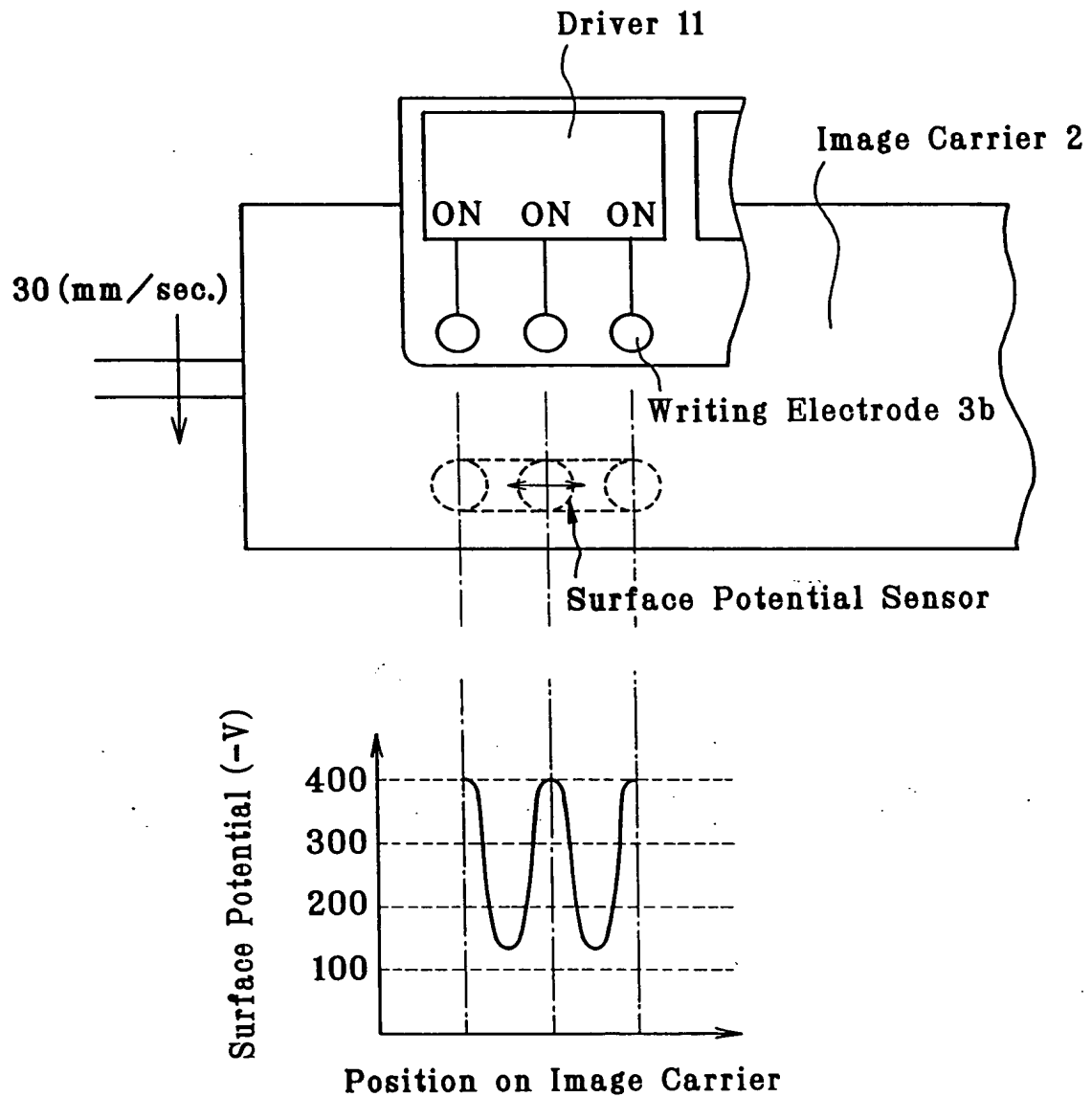


FIG. 15

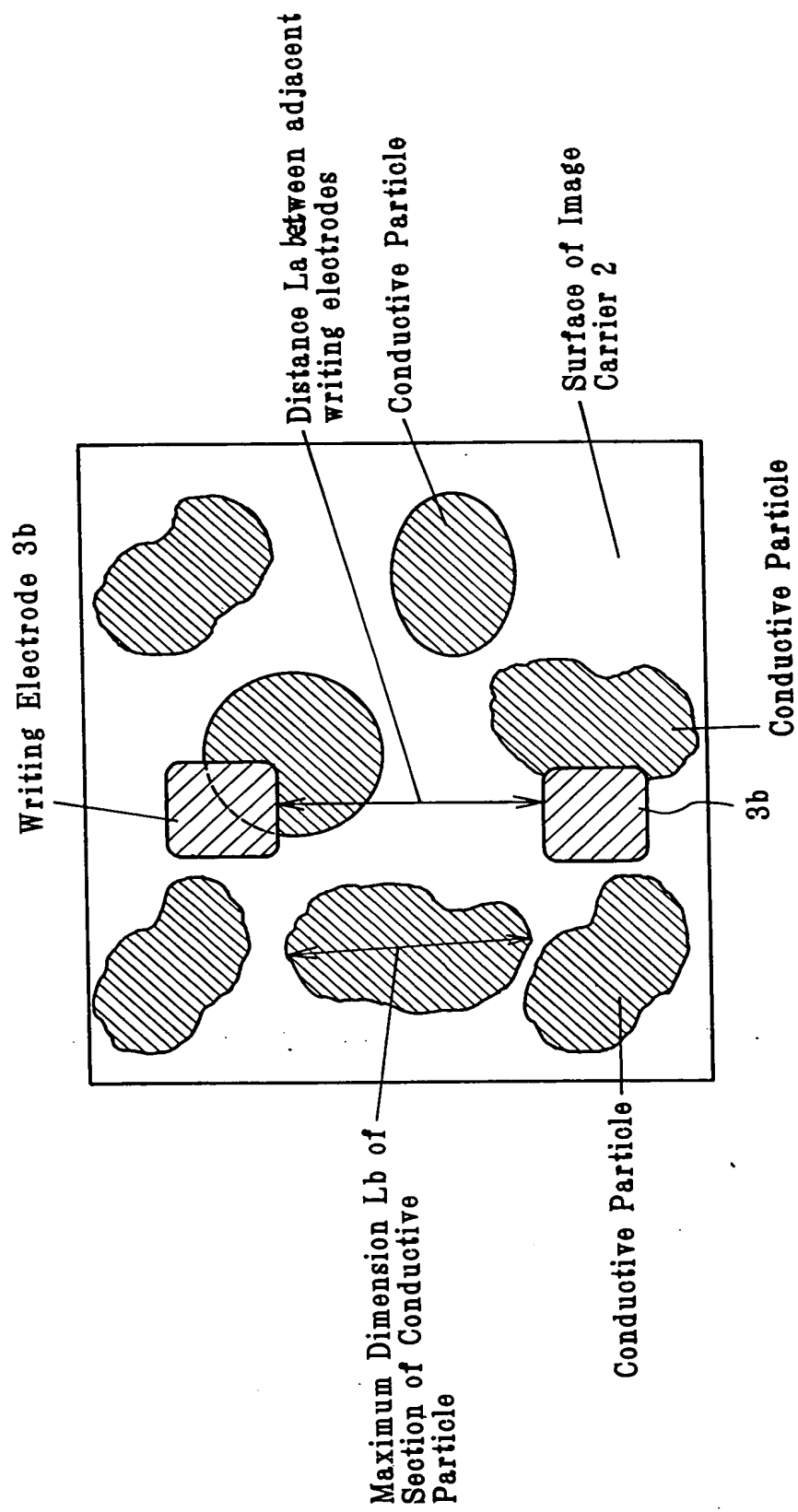


FIG. 16(a)

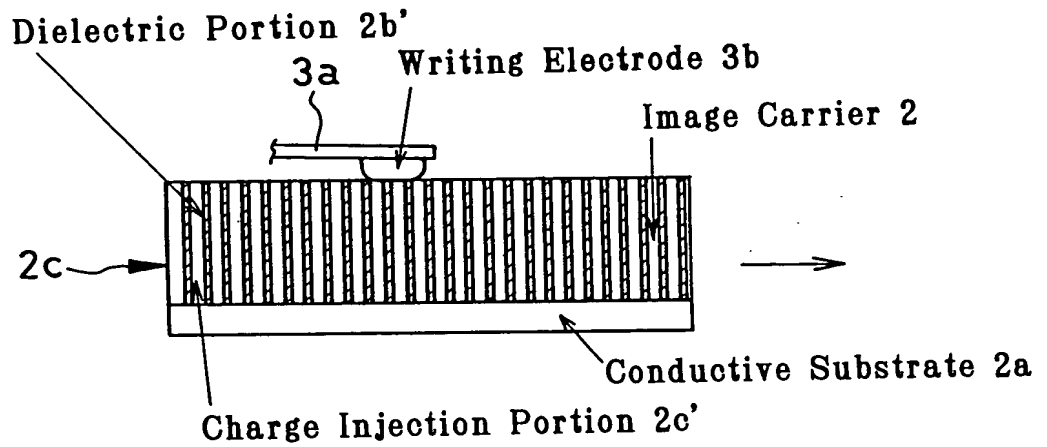


FIG. 16(b)

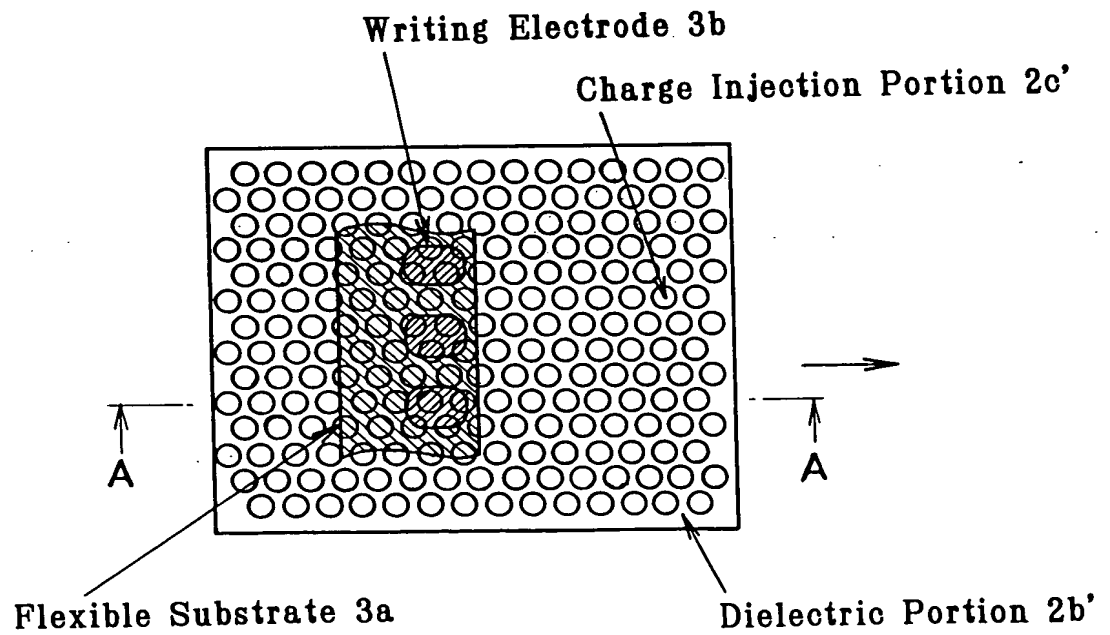


FIG. 17(a)

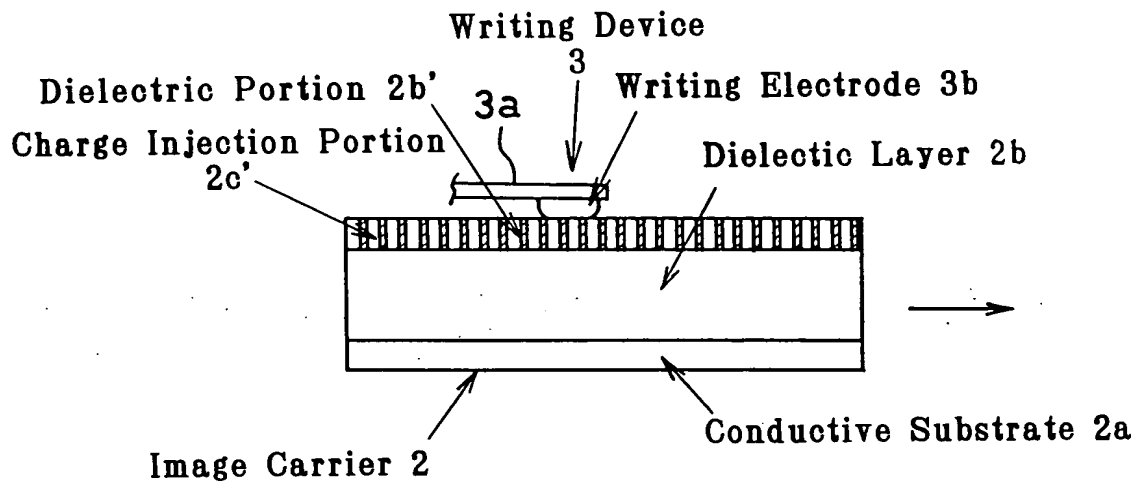
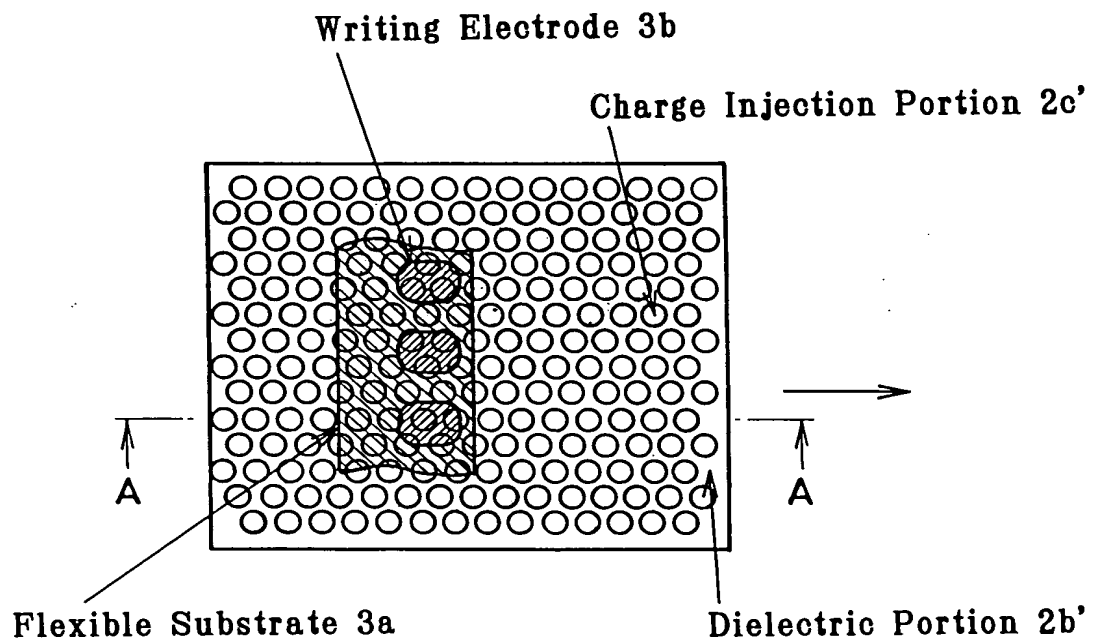
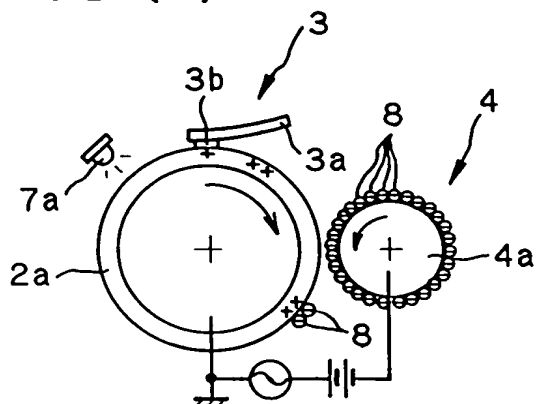


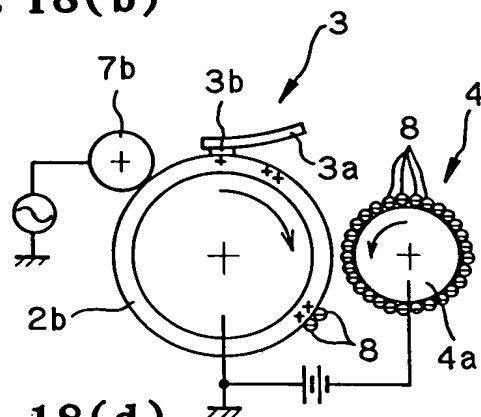
FIG. 17(b)



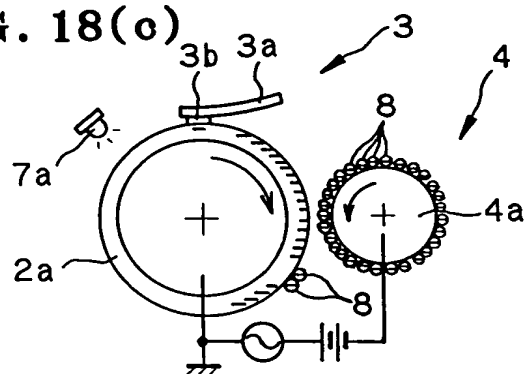
**FIG. 18(a)**



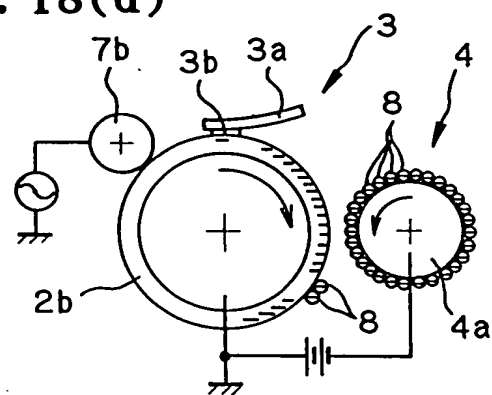
**FIG. 18(b)**



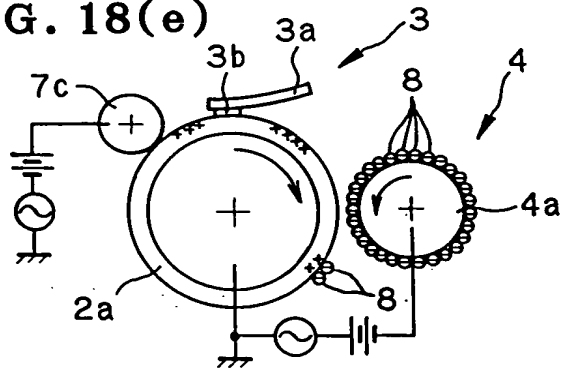
**FIG. 18(c)**



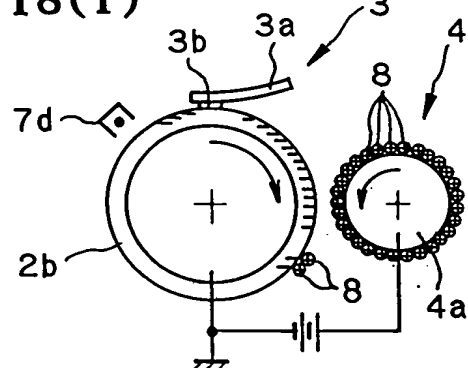
**FIG. 18(d)**



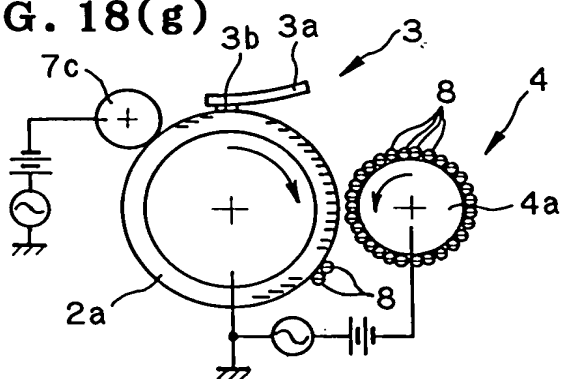
**FIG. 18(e)**



**FIG. 18(f)**



**FIG. 18(g)**



**FIG. 18(h)**

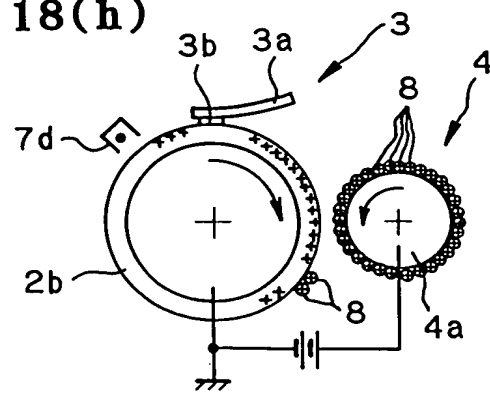


FIG. 19(A)

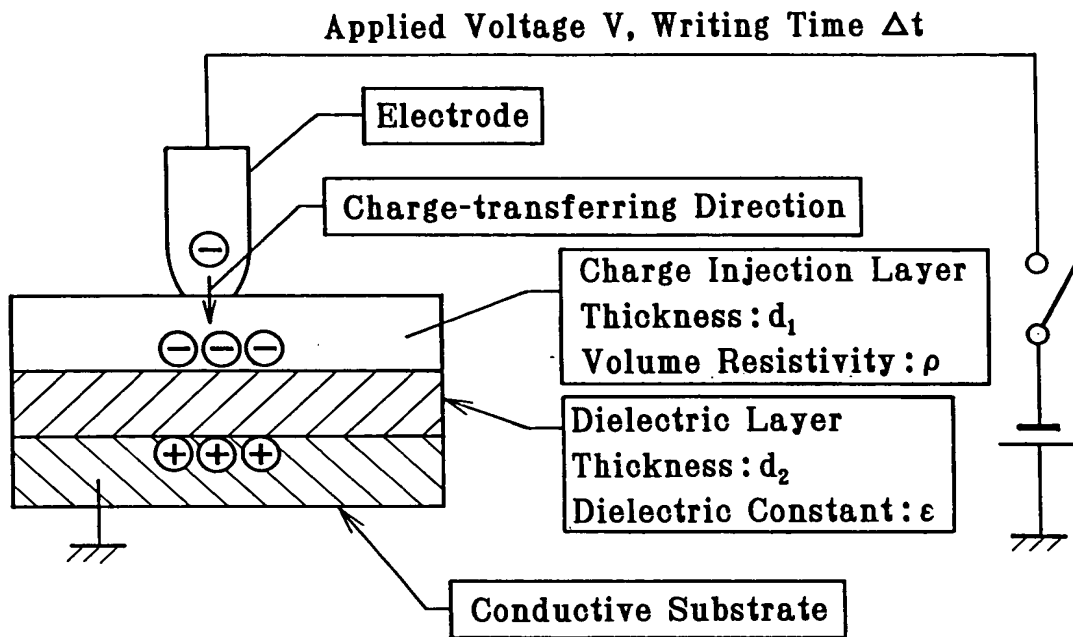


FIG. 19(B)

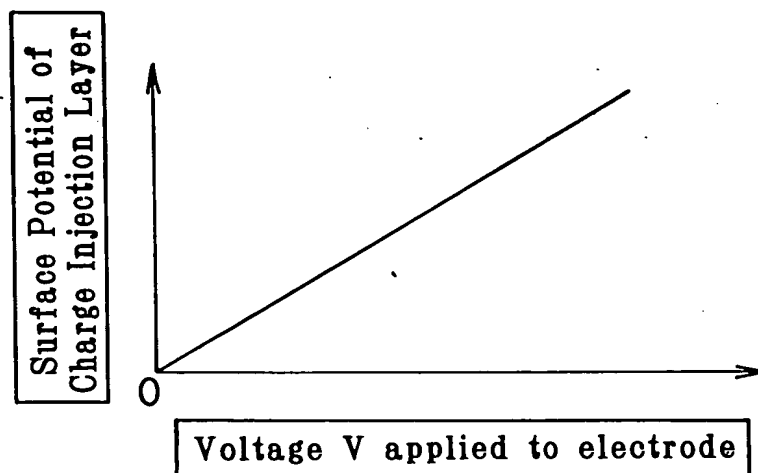


FIG. 20(A)

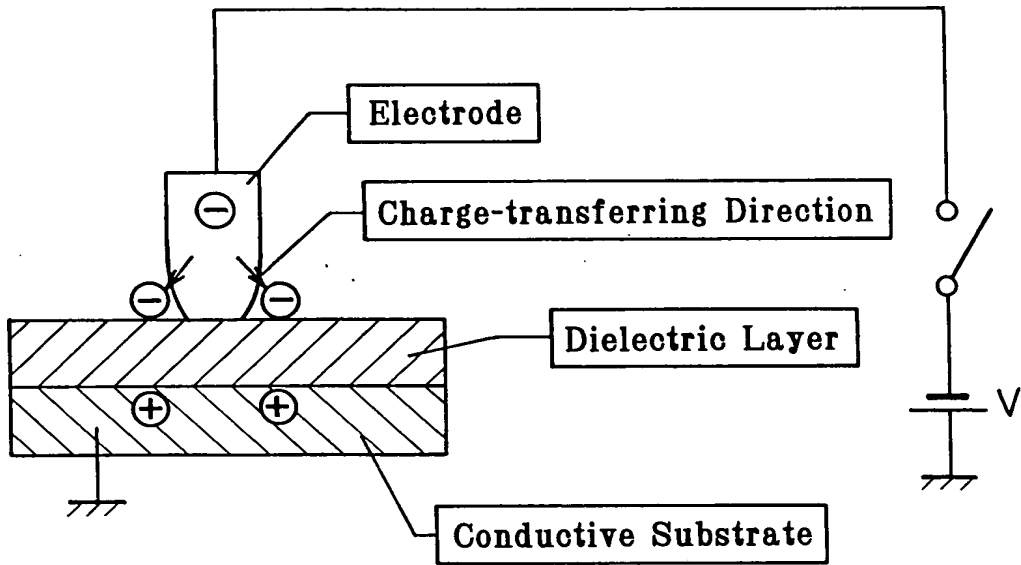


FIG. 20(B)

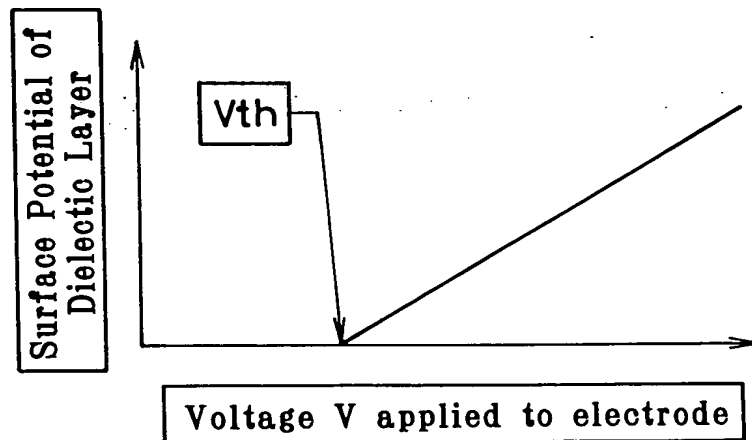


FIG. 21

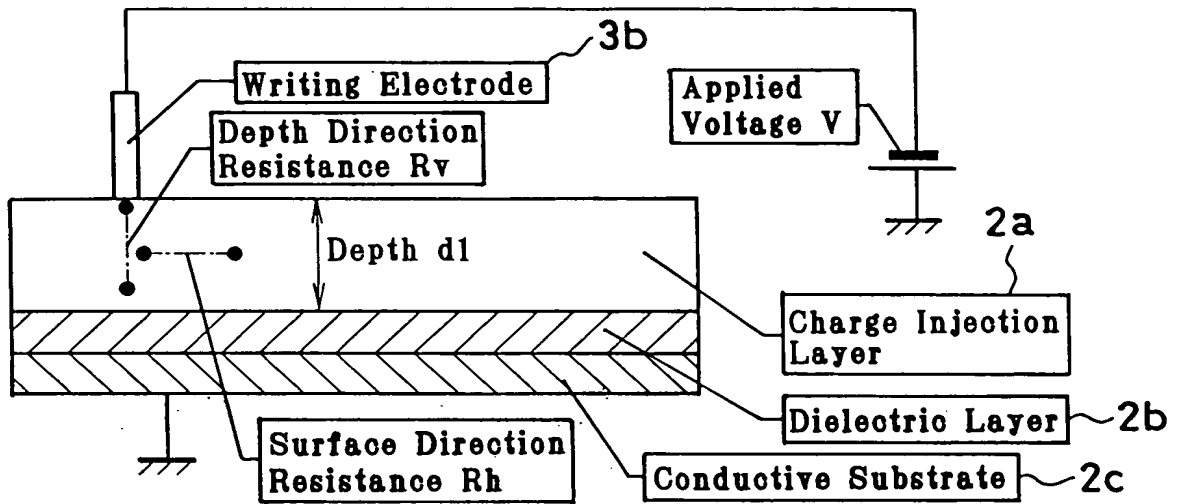


FIG. 22

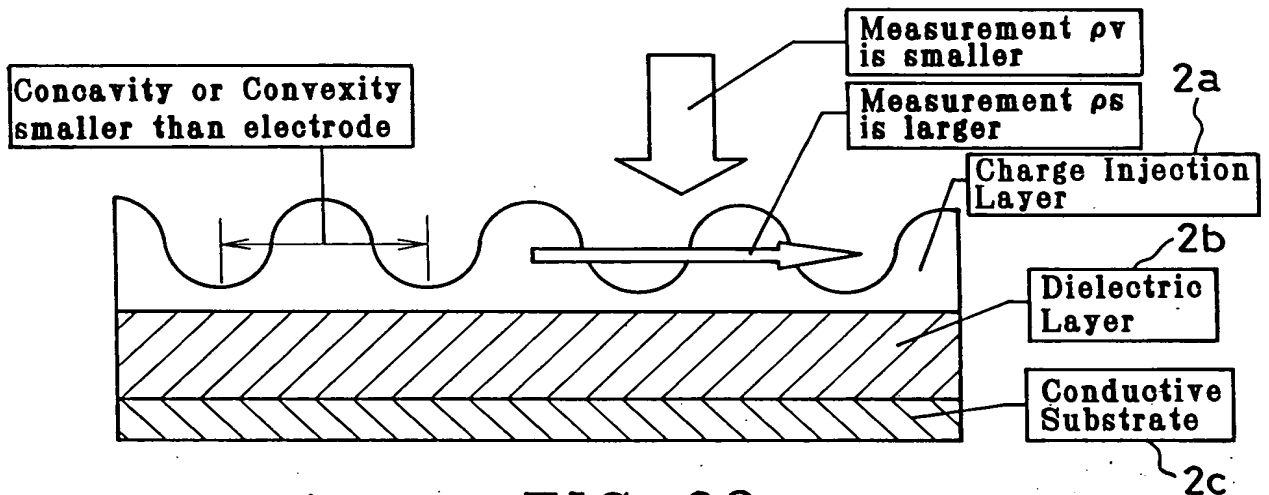


FIG. 23

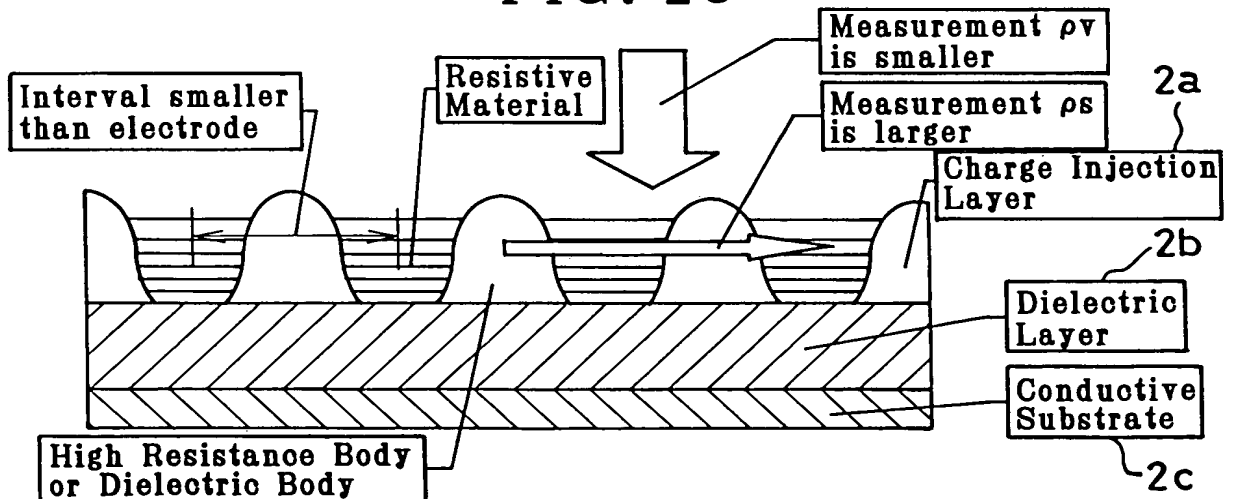


FIG. 24(A)

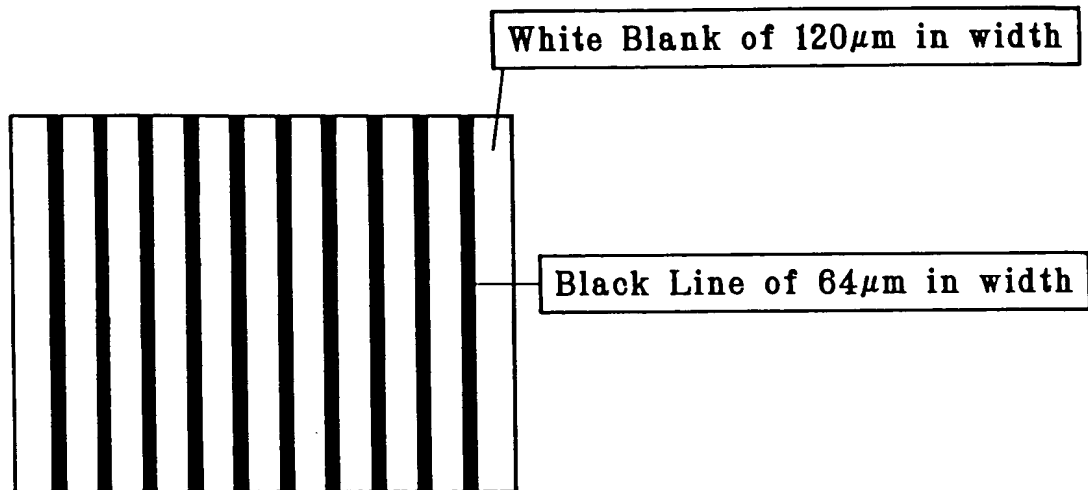


FIG. 24(B)

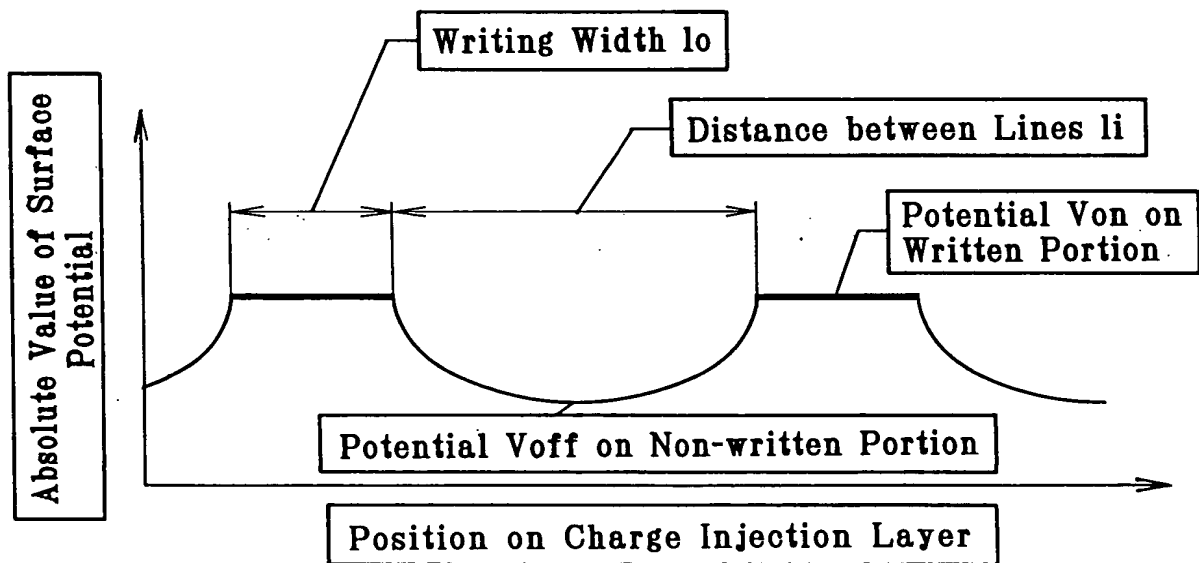


FIG. 25(A)

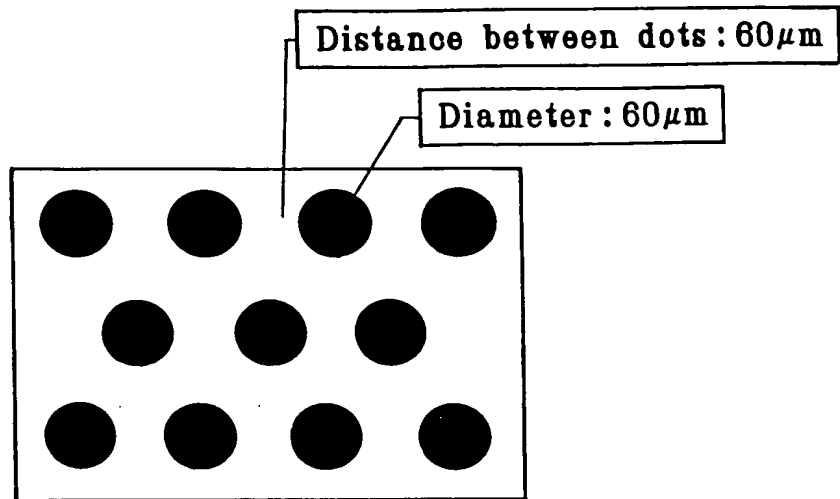


FIG. 25(B)

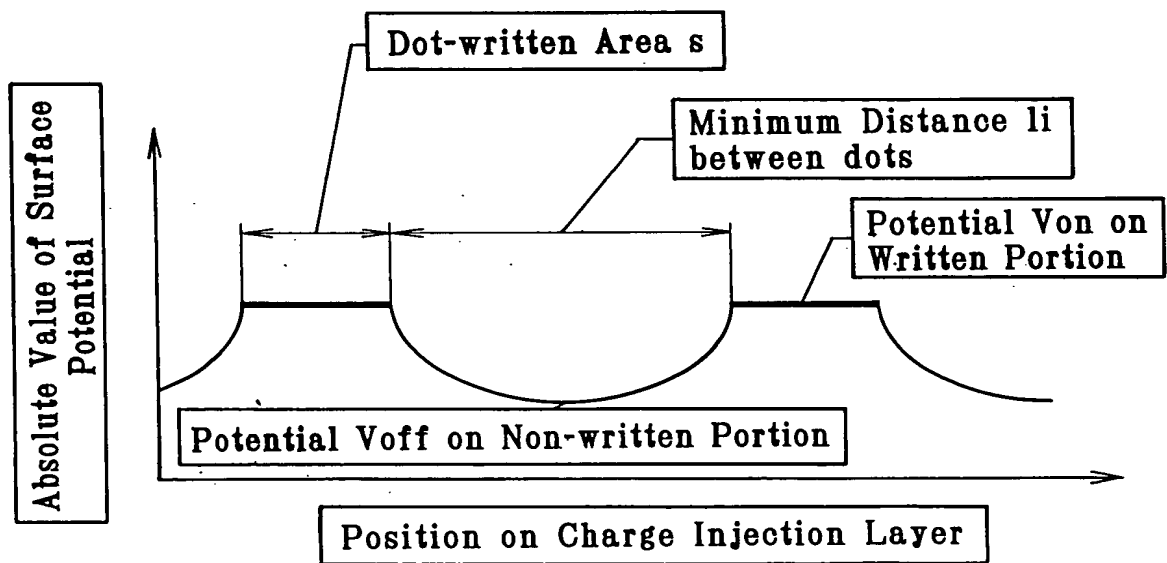


FIG. 26(A)

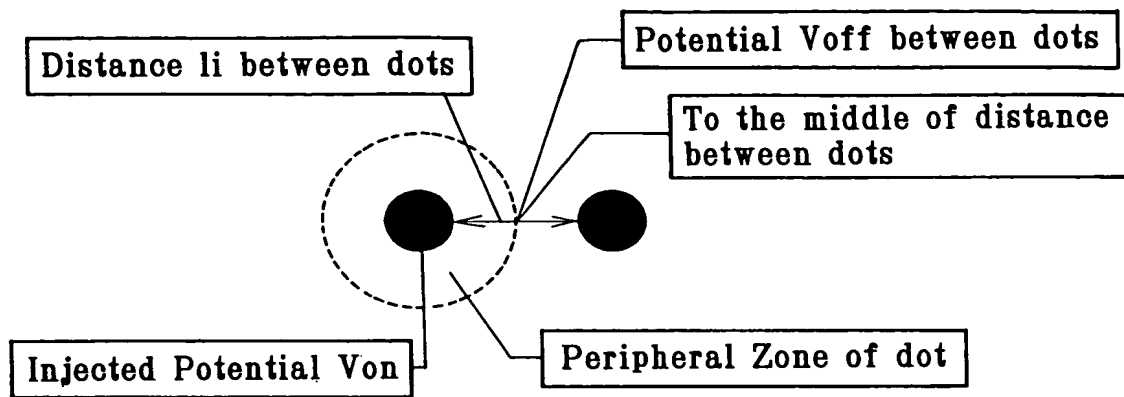


FIG. 26(B)

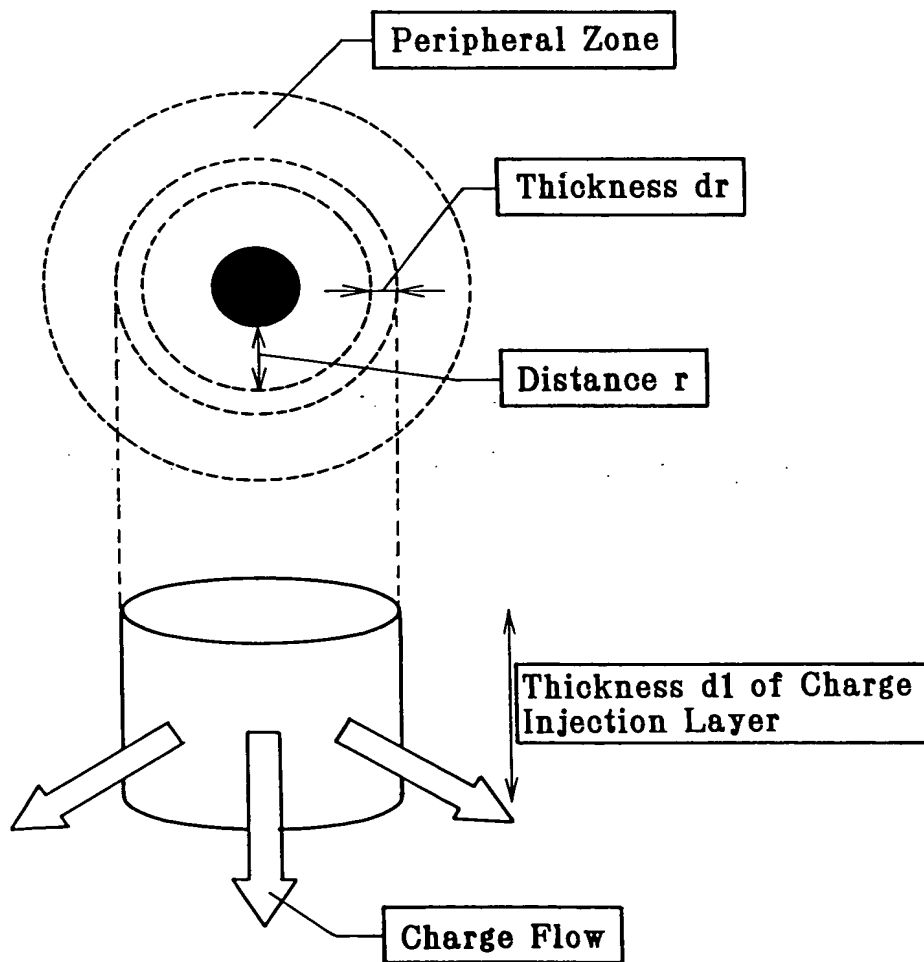


FIG. 27(A)

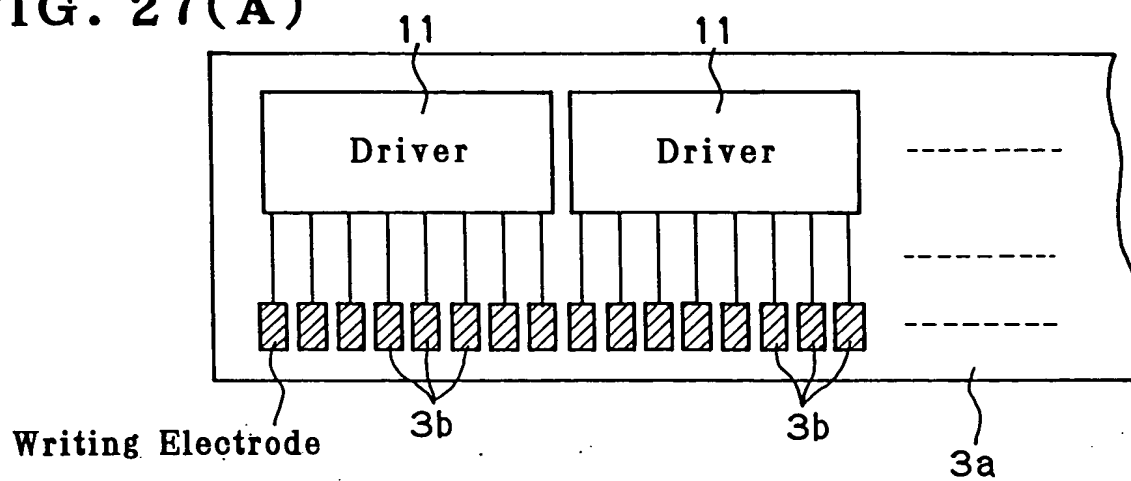


FIG. 27(B)

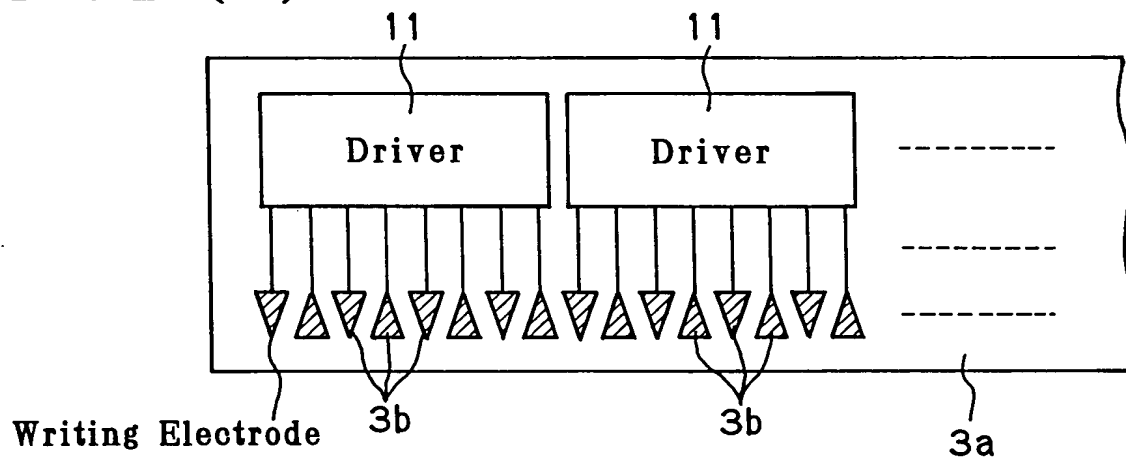


FIG. 27(C)

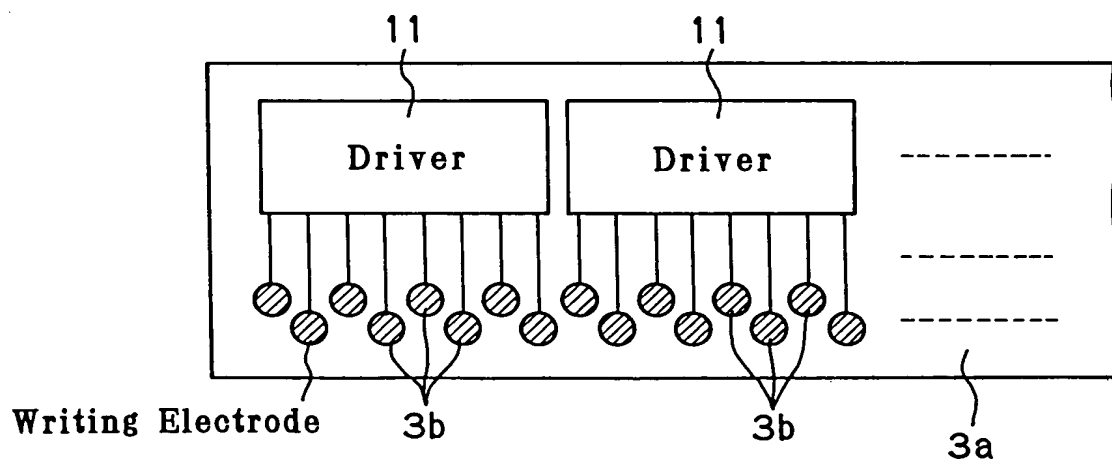


FIG. 28(A)

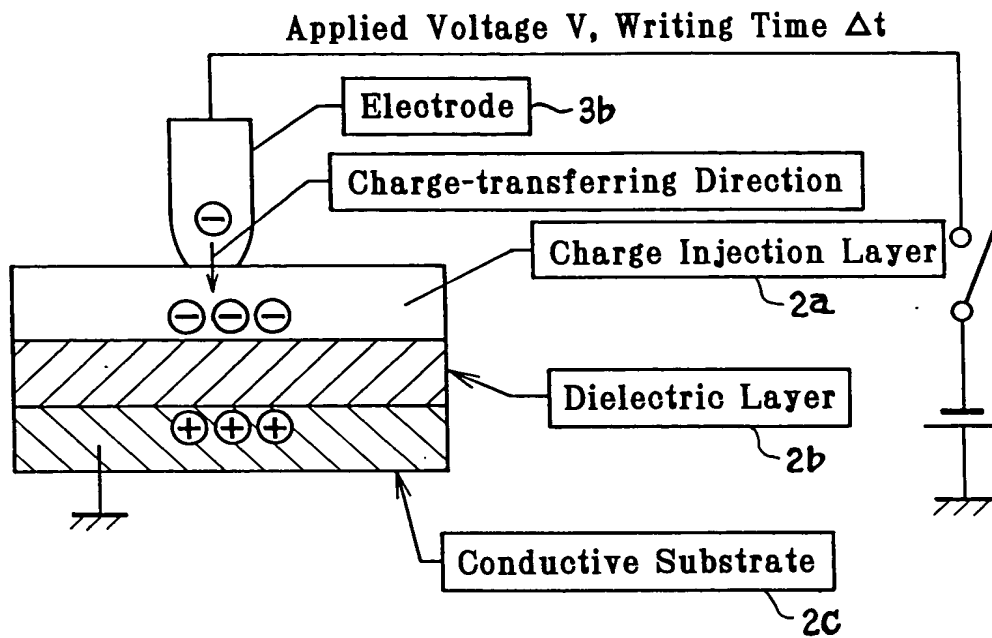


FIG. 28(B)

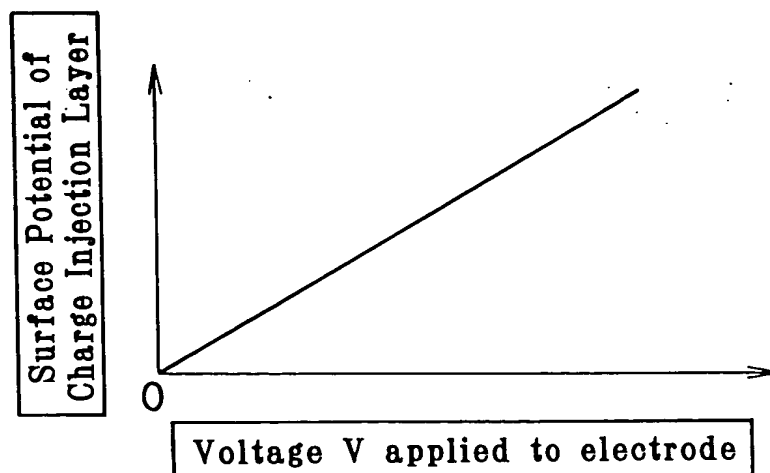


FIG. 29

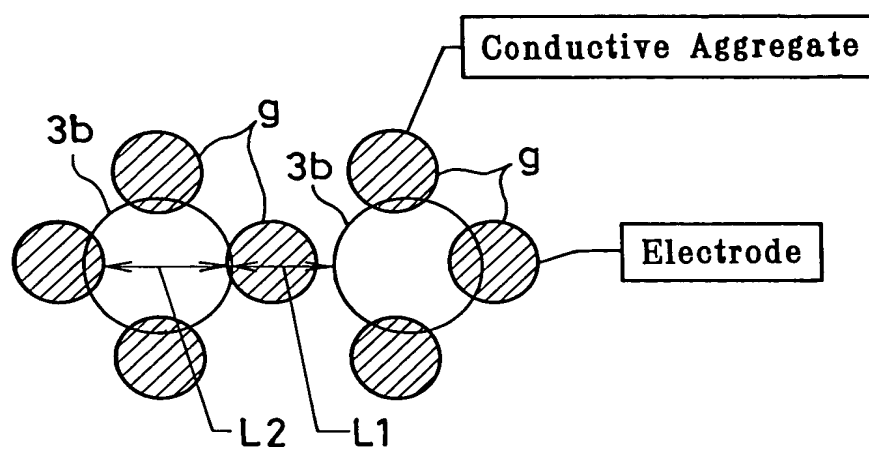


FIG. 30(B)

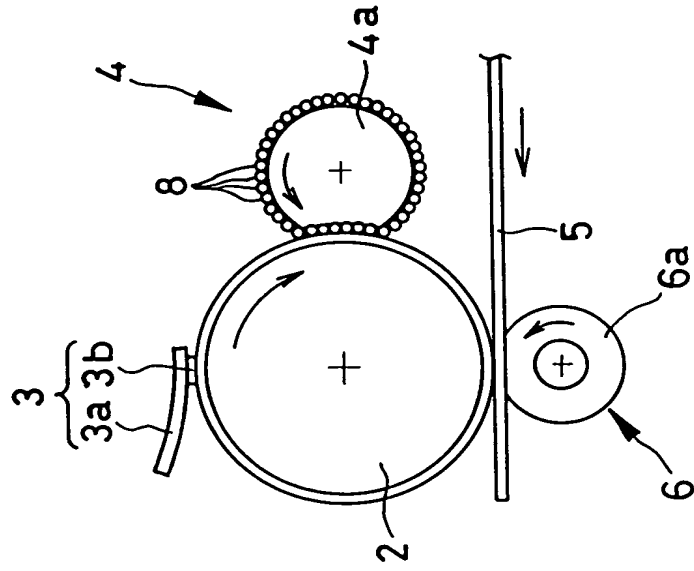


FIG. 30(A)

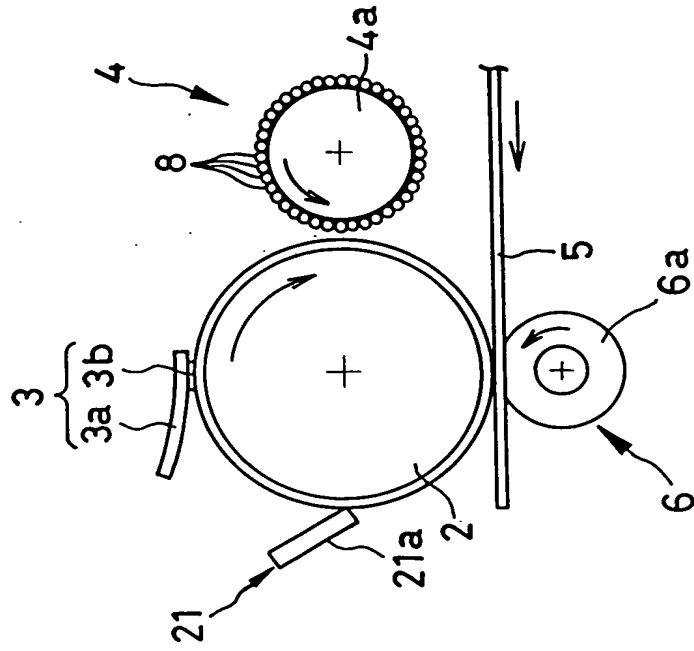
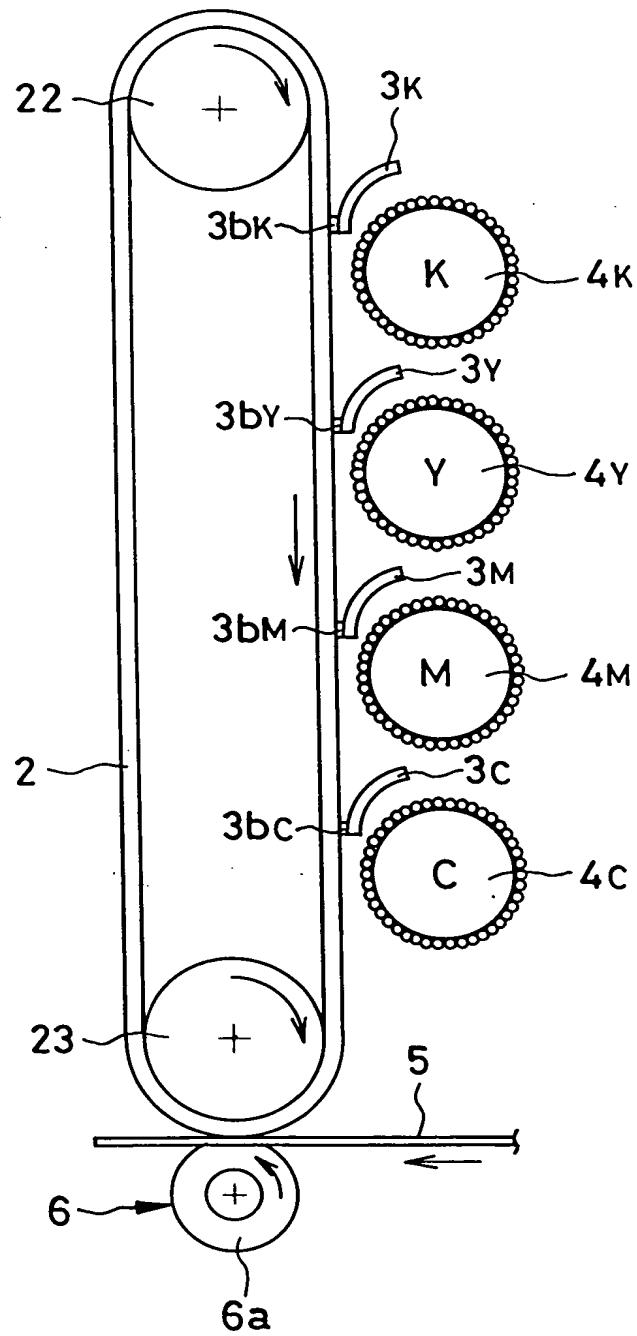


FIG. 31



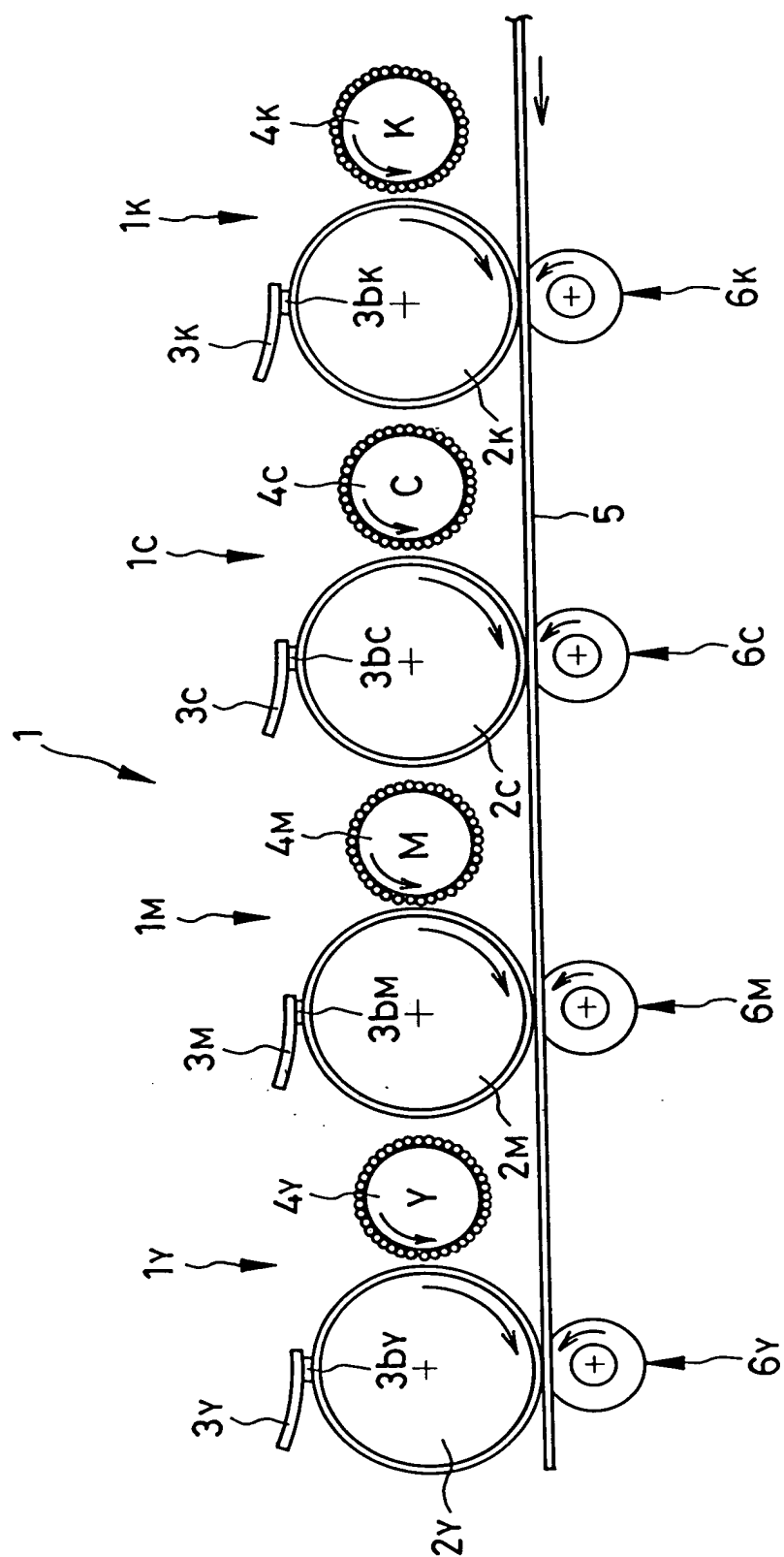


FIG. 33

